

Hysteretic Controlled DC-DC Buck Converter with 2.8V to 4.2V Battery Supply for Portable Applications in 90nm CMOS

Abstract:

The proposed invention is a DC-DC buck converter designed in a lower technology 90nm mixed mode CMOS which can be integrated along with the digital processor. It uses Pulse width modulation (PWM) and pulse frequency modulation (PFM) modes of control with an internal power management unit to provide supply for the stacked transistors and the control circuits.

Patent Application No. 3257/MUM/2012